

KLEIN, O'NEILL & SINGH LLP

2 PARK PLAZA, SUITE 510

IRVINE, CA 92614, USA

TEL: 949-955-1920

FAX: 949-955-1921

WWW.KOSLAW.COM

*called & left message
with secretary
for schedule
in interview
5/17/04*

FACSIMILE TRANSMITTAL SHEET

TO:	Kim T. Huynh	FROM:	TJ Singh
COMPANY:	USPTO	DATE:	05/01/2004
FAX NUMBER:	7037469224	TOTAL NO. OF PAGES INCLUDING COVER:	5
PHONE NUMBER:	703-305-5384	SENDER'S REFERENCE NUMBER:	TDIM03-PATUS
RE:	S/NO. 09/490,263	YOUR REFERENCE NUMBER:	None

Call 25 11A 11

☒ URGENT ☐ FOR REVIEW ☐ PLEASE COMMENT ☒ PLEASE REPLY ☐ PLEASE RECYCLE

Dear Ms. Huynh,

I am faxing you the request for telephone interview in this case. Please let me know what time works for you.

Regards,

TJ Singh

The contents of this facsimile message, including any attachments, are intended solely for the use of the person or entity to which the fax was addressed. It contains information that may be protected by the attorney-client privilege, work-product doctrine, or other privileges, and may be restricted from disclosure by applicable state and federal law. If you are not the intended recipient of this fax, be advised that any dissemination, distribution, or use of the contents of this facsimile message is strictly prohibited. If you have received this fax in error, please contact TJ Singh at 949-955-1921. Thank you.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Jing Wang, et al.
ASSIGNEE: TransDimension International Corp.
APPLICATION SERIAL NO.: 09/490,263
FILING DATE: 01/24/2000
TITLE : System for Transferring Data Using a USB Host System
with a Dedicated Processor
EXAMINER: Kim T. Huynh
GROUP ART UNIT: 2189
ATTY. DOCKET NO: TDIM03PAT-US

COMMISSIONER FOR PATENTS
PO BOX 1450
ALEXANDRIA, VA 22313-1450

REQUEST FOR TELEPHONE INTERVIEW FOR THE OFFICE ACTION DATED**2/24/2004**

*Que, 11 AM
251... 949-370-4451*

Dear Sir:

With respect to the 02/24/2004 Office Action in the above-referenced Application, Applicants request a telephone interview with the Examiner. Applicants propose the following amendments to the independent claims with the following agenda for the conference.

PROPOSED INDEPENDENT CLAIMS:

1. (Currently Amended) A USB host system operationally coupled to a host computing system with a main processor such that the USB host system is located on the host computing side and allows connectivity to a USB device, comprising:

a first processor that implements a USB driver without using the main processor resources; *and a host controller w/o using the main processor resource*

a downstream USB port; and,

a communication area directly accessible by both the main processor and the first processor such that the first processor interfaces with the main processor via the communication area using predefined records in pre-defined formats,

applicant will review

wherein the main processor writes a data transfer request in the communication area in a pre-defined record format, and

wherein the first processor schedules and completes the request via a USB host controller.

11. (Currently Amended) A USB host system operationally coupled to a host computing system such that the USB host system is located on the host computing side and allows connectivity to a USB device, comprising:

a first processor implementing a function for managing a USB host controller with or without an operating system running on the computing system;

a downstream USB port; and,

an interface between the first processor and a second processor that provides a high-level USB pipe view of a USB system to an application program running on the second processor in the computing system,

wherein the interface comprises a memory that is directly accessed by both the first and second processors, and

wherein the second processor interfaces with the host system via a standard microprocessor bus.

Other independent claims will be amended similar to Claim 1 and 11.

Proposed Agenda:

Applicants propose the following agenda for the telephone conference:

- a. Discuss the foregoing proposed claim amendment.
- b. Discuss the prior art cited by the Examiner, Larky et.al. (US Patent No. 6,389,495) based on at least the following:

Applicants' position is that Larky does not disclose a **USB host system** of the present invention, and instead discusses a USB dedicated circuit that is located in a peripheral device. This is shown in Figure 1 of Larky where the USB dedicated circuit 124 is a part of a device controller 104 and located in peripheral device 101. This is different than the USB host system of the present invention.

Applicants would like to direct the Examiner's attention to Figure 3 of the present application. In Figure 3, the USB host system 300 is coupled to main processor 310 and the USB devices 320 are coupled to USB host system. Larky's device 101 is one of the USB devices 320 each having the USB dedicated circuit 124 and is not the USB host system 300 (or 200).

Applicants also direct the Examiner's attention to Figure 2 of the present application. In Figure 2, the USB host system 200 has a processor 210, which is the first processor of Claim 1. Processor 210 runs the USB driver. This USB driver is run on the host side and not on the peripheral side (for example, in device 101(Figure 1 of Larky)). Based on the foregoing, Larky's processor 126 (Figure 1) as cited by the Examiner is different from the first processor 210 (Figure 2) of the present invention. Larky's processor 126 is located in the peripheral device 101 and is not a part of a USB host system as shown in Figure 2 and 3 of the present application. Processor 126 in Larky is a part of a USB dedicated circuit 124 that is located at the opposite end of the architecture (i.e. on the peripheral side) compared to the first processor 210 of the present invention, which is located on the host side.

Also, Larky's down stream USB port 106 (Figure 1) is not the same as the "downstream ports" referred to in Claim 1 of the present application. USB 106 in Larky is a Universal Serial Bus (USB) that connects computer system 100 to device controller 104 of a peripheral device 101. {Larky, Col. 3, Lines 49-52}. The downstream USB ports of Claim 1 of the present invention refer to USB ports 245 of Figure 2 that couple various USB devices (for example 320 of Figure 3 of the present application) to USB host system 200.

Furthermore, Larky does not disclose a USB host system (Fig. 2, 200) of the present invention with a direct access between the "communication area," i.e., the "DCM" (Fig. 2, 220) and both the "main processor" (Fig. 3, 310; p. 8, lines 16-19) and the dedicated "first processor" (Fig. 2, 210).

The Examiner has compared DCM 220 with RAM 122 in Figure 1 of Larky. RAM 122 of Larky is different from DCM 220 and does not perform the same function as DCM 220. RAM 122 is located within the peripheral device 101, unlike the DCM 220 of the present invention that is a part of USB host system 200 (Figure 2 of the present application).

In light of the foregoing distinctions, it is respectfully submitted that the Examiner's rejection of Claims 1 under 35 U.S.C. 102(e) in view of Larky is untenable and must be withdrawn.

Based on the foregoing, Applicants respectfully request a telephone interview with the Examiner.

I hereby certify that this correspondence is being FAXED on 5/6/04, to fax number 703-746-9224.

Tejinder Singh
Attorney for Applicant(s)

Date of Signature

5/6/04

Respectfully submitted,



Tejinder Singh
Attorney for Applicants
Reg. No. 39,535
Tel No: 949-955-1920